

Claim Amendments

Applicants have amended claims 1, 6, 12, 15, 20, 26 and 29-32. Applicants set forth below a complete listing of the claims with the corresponding status indicated for each claim.

1. (Currently Amended) A method for mitigating defects caused by inoperative pixels in a liquid crystal micro-display built on a silicon integrated circuit substrate, said the substrate having an integral complimentary metal-oxide semiconductor (CMOS) control chip containing CMOS drive circuitry, the drive circuitry comprising a plurality of pixel drive circuits, each pixel drive circuit coupled to a corresponding pixel, the method comprising:

~~fully manufacturing the control chip;~~

~~identifying a defective CMOS pixel drive circuitry for the circuit coupled to an inoperative pixel;~~

disconnecting the defective pixel drive circuitry circuit from the inoperative pixel; and

connecting the inoperative pixel to a working pixel drive circuit coupled to [[of]] a nearby pixel such that the defective pixel drive circuitry circuit is bypassed and the inoperative pixel is driven from the working pixel drive circuit of [[a]] the nearby pixel, said the nearby pixel comprising one of an adjacent pixel or a non-adjacent pixel.

2-3. (Cancelled).

4. (Previously presented) A method in accordance with claim 1, wherein connecting comprises using a bypass bit latch comprising a bypass bit, and wherein the bypass bit is loaded from an external memory after the display is turned on.

5. (Previously presented) A method in accordance with claim 1, further comprising: multiplexing the drive circuits of each pixel with the drive circuit of a nearby pixel.

6. (Currently Amended) A method in accordance with claim 1, wherein connecting comprises using a bypass bit latch comprising a bypass bit, and wherein the method further comprises:

providing a tri-state transistor associated with each pixel which is connected to the bypass bit latch; and

providing a resistor for coupling neighboring pixels;

such that when the bypass bit is set, the transistor is switched to bypass the defective pixel drive circuitry circuit so that the inoperative pixel is driven from the working pixel drive circuit of a nearby pixel through the resistor.

7-10. (Cancelled).

11. (Previously Presented) A method in accordance with claim 1, wherein defects of the inoperative pixels are mitigated in groups.

12. (Currently Amended) A method in accordance with claim 1, wherein identifying defective pixel drive circuitry circuit further comprises ~~the further step of~~ providing test circuitry associated with the display.

13. (Original) A method in accordance with claim 1, wherein the pixel drive circuitry associated with each pixel is located separately from each pixel.

14. (Cancelled).

15. (Currently Amended) A liquid crystal micro-display apparatus capable of mitigating defects caused by inoperative pixels, comprising:

a plurality of pixels;
a ~~fully manufactured~~ complimentary metal-oxide semiconductor (CMOS) control chip integral to a silicon integrated circuit substrate of said the micro display, said the CMOS control chip containing CMOS drive circuitry for controlling the pixels comprising a plurality of pixel drive circuits, each pixel drive circuit coupled to a corresponding pixel,

means for disconnecting a defective ~~CMOS~~ drive circuitry pixel drive circuit from an inoperative pixel; and

a means for connecting the inoperative pixel to a working pixel drive circuit of a nearby pixel, such that the defective pixel drive ~~circuitry~~ circuit is bypassed and the inoperative pixel is driven from the working pixel drive circuit of [[a]] the nearby pixel, said the nearby pixel comprising one of an adjacent pixel or a non-adjacent pixel.

16-17. (Cancelled).

18. (Previously presented) Apparatus in accordance with claim 15, wherein the means for connecting comprises a bypass bit latch comprising a bypass bit that is loaded from an external memory after the display is turned on.

19. (Previously presented) Apparatus in accordance with claim 15, further comprising: multiplexing circuitry associated with the connecting means.

20. (Currently Amended) Apparatus in accordance with claim 15, wherein the means for connecting comprises a bypass bit latch comprising a bypass bit, and wherein the apparatus further comprises:

a tri-state transistor associated with each pixel connected to the bypass bit latch; and

a resistor coupling neighboring pixels;

such that when the bypass bit is set, the transistor is switched to bypass the defective pixel drive ~~circuitry~~ circuit so that the inoperative pixel is driven from the working pixel drive circuit of a nearby pixel through the resistor.

21-24. (Cancelled).

25. (Previously Presented) Apparatus in accordance with claim 15, wherein defects of the inoperative pixels are mitigated in groups.

26. (Currently Amended) Apparatus in accordance with claim 15, further comprising test circuitry to identify the defective pixel drive circuitry circuit.

27. (Original) Apparatus in accordance with claim 15, wherein the pixel drive circuitry associated with each pixel is located separately from each pixel.

28. (Cancelled).

29. (Currently Amended) A method in accordance with claim 1, wherein said the defective CMOS pixel drive circuitry circuit is identified after the CMOS control chip and the liquid crystal material are assembled together.

30. (Currently Amended) A method in accordance with claim 29, wherein said the inoperative pixel is identified via an optical inspection of the display after assembly of said the display.

31. (Currently Amended) Apparatus in accordance with claim 15, wherein the defective CMOS pixel drive circuitry circuit is identified after the CMOS control chip and the liquid crystal material are assembled together.

32. (Currently Amended) Apparatus in accordance with claim 31, wherein said the inoperative pixel is identified via an optical inspection of the display after assembly of said the display.